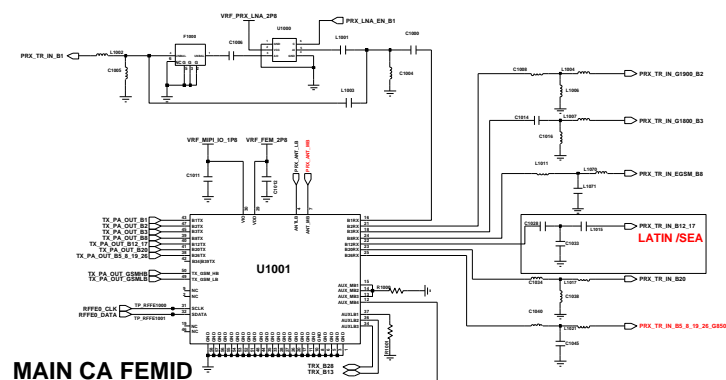
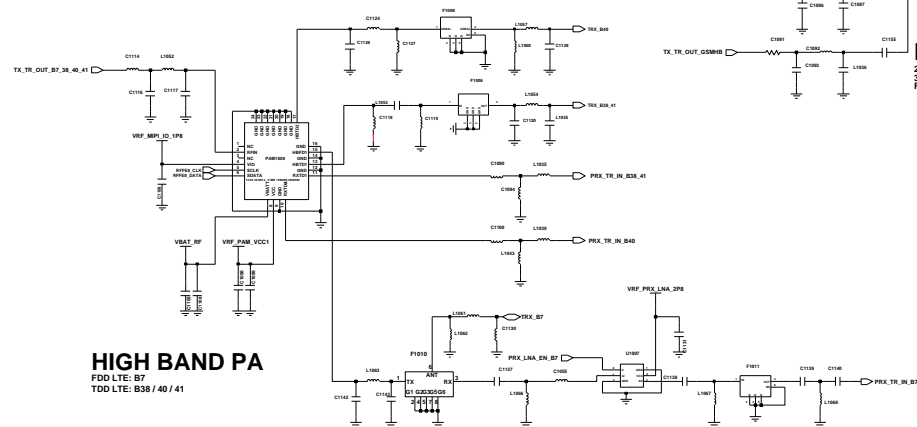


[illegible][illegible][illegible][illegible][illegible][illegible][illegible][illegible][illegible][illegible]

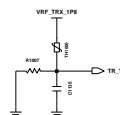



LATIN /SEA



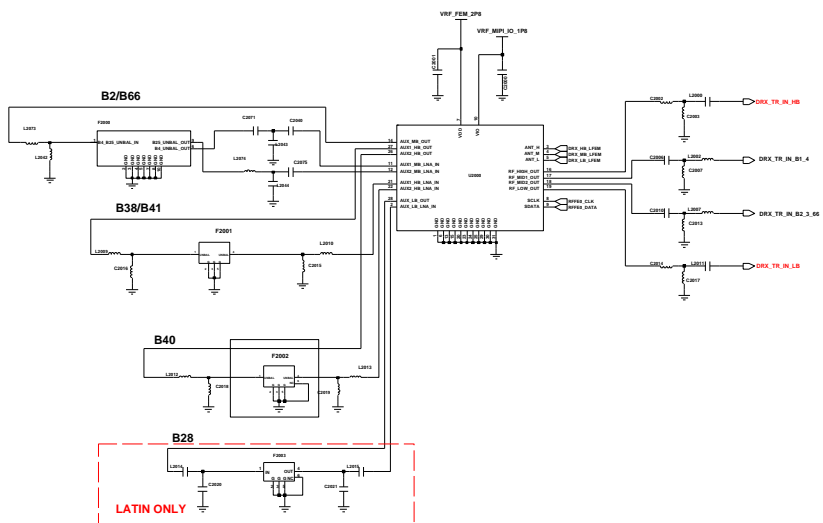
FDD LTE: B7
TDD LTE: B38 / 40 / 41

RF THERMISTOR

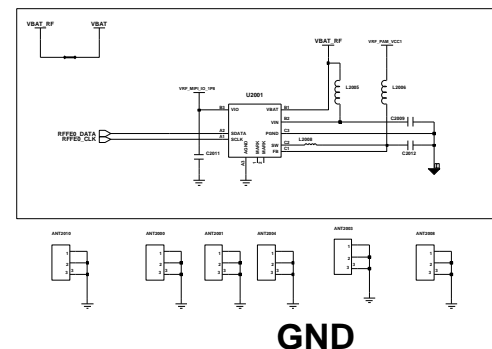


Engineer:	SEA			
Drawn by:	SEA			
RDG CAC:	DATE:			Rev
DOC CTRL CHK:				10-5-10
INFO ENGR CHK:				
QA CAC:	REV	Issuing Number:	Page	
15		50		

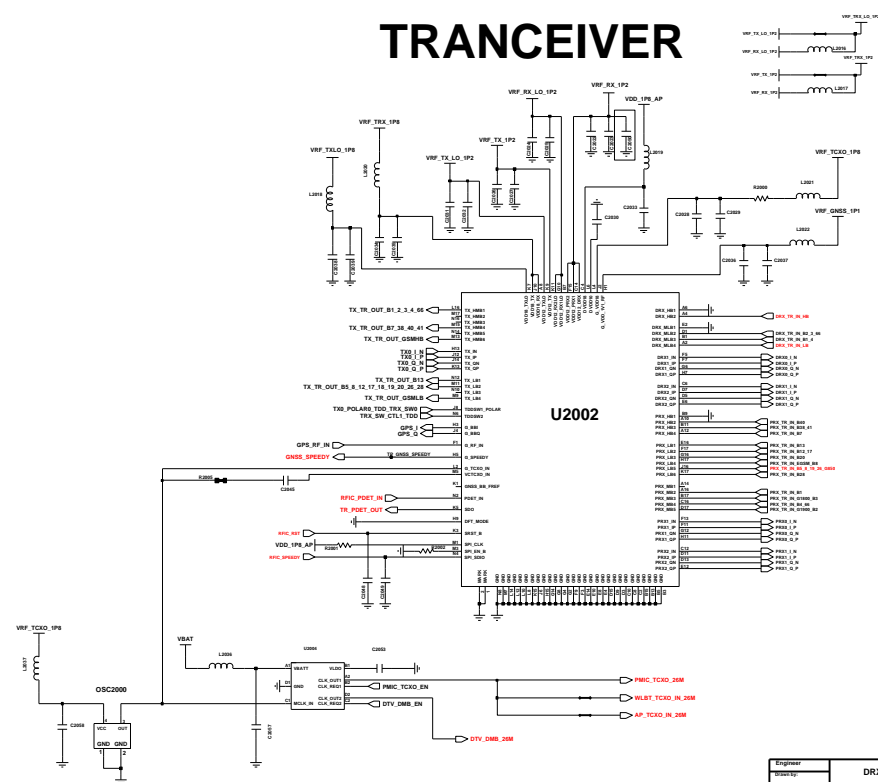
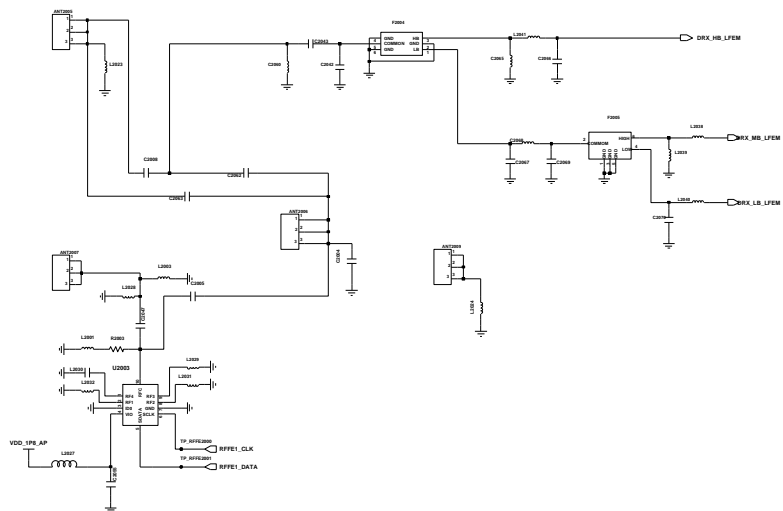
DRX



APT MODULATOR



TRANSEIVER



Engineer	DRX, TRANSEIVER
Designer	
Checker	
Reviewer	
Approved	
Drawn	
Checked	
Reviewed	
Approved	

BT/WIFI

NFC

GPS

SENSORS

DMB (for KOR)

DTV

Option for GT

NFC	NFC PART	Power supply of SIM		NFC GPIO 14	
	U3004	R5030	R5031	R4012	R4013
NFC	CONNECT	short	open	10K ohm	open
nonNFC	NC	open	short	open	10K ohm

SENSOR LDO

Proxi Sensor

6 AXIS(ACC+GRYO)

MAGNETIC

SENSOR	SENSOR, WIF/BT/GPS/NFC
TEST DATE	
TEST ENGINEER	
TEST RESULT	

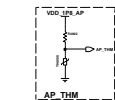
EXYNOS7904 (Lassen-O Plus)

POP LP4 3G : SEC

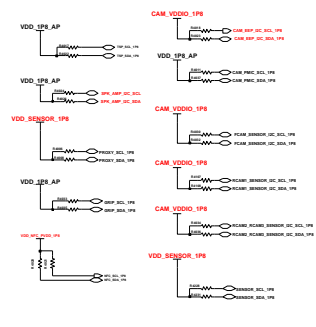
AP JTAG BOM NC

BOOT option (need to update for Lassen)

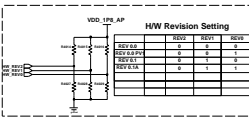
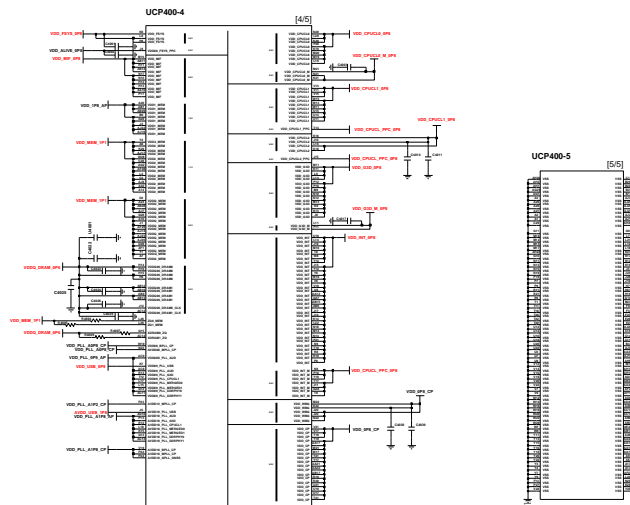
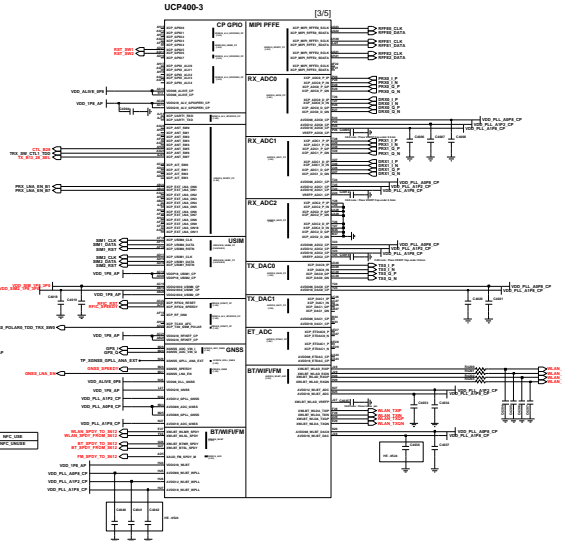
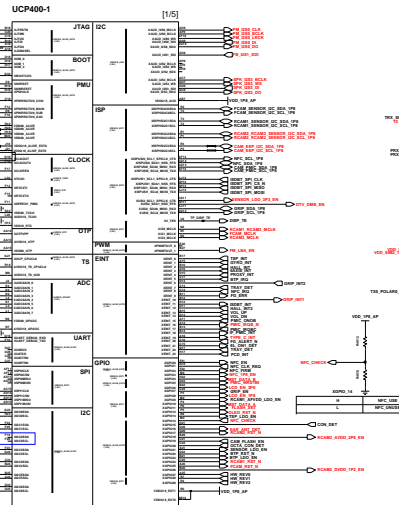
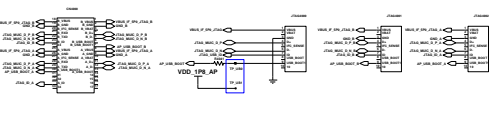
COMPONENT	REF
BOOT	BOOT
BOOT	BOOT
BOOT	BOOT
BOOT	BOOT



I2C Pull-up Register

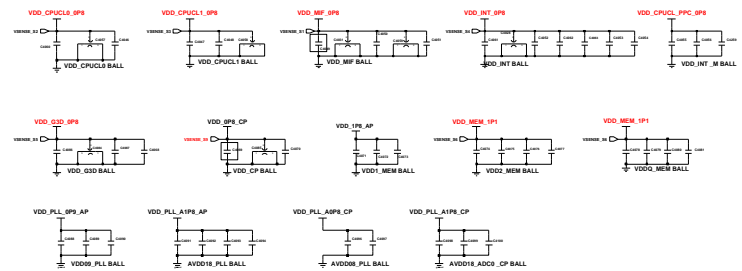


PCB JTAG

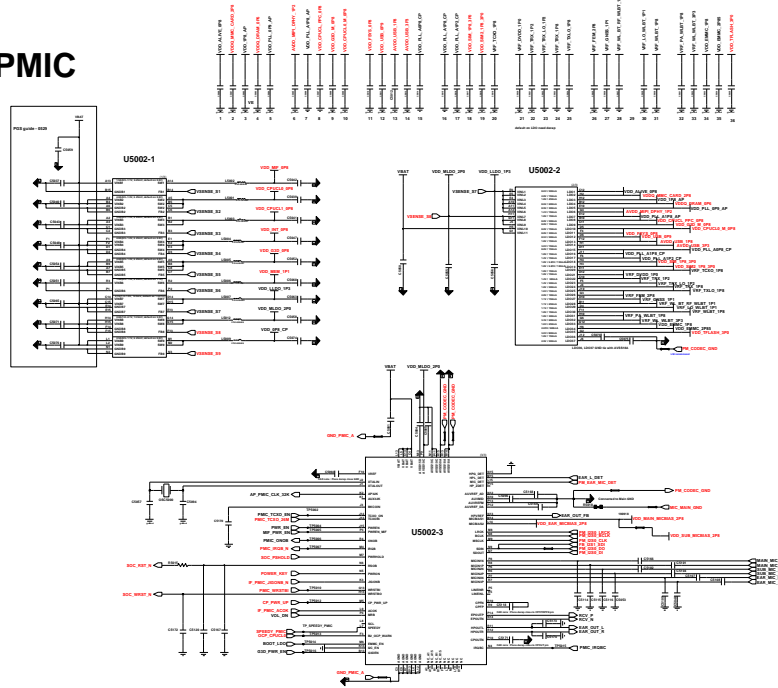


DDR	eMMC
3G	32G
4G	64G
6G	128G

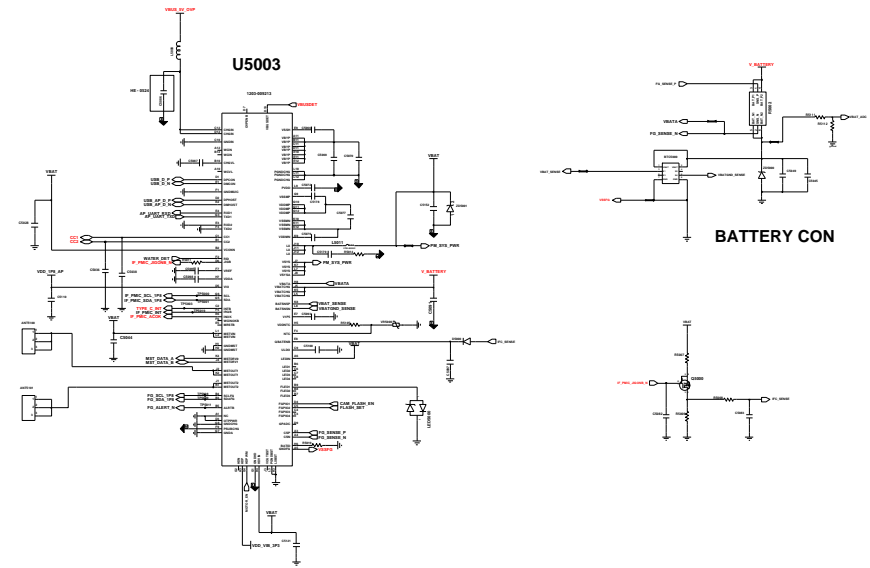
AP Decap



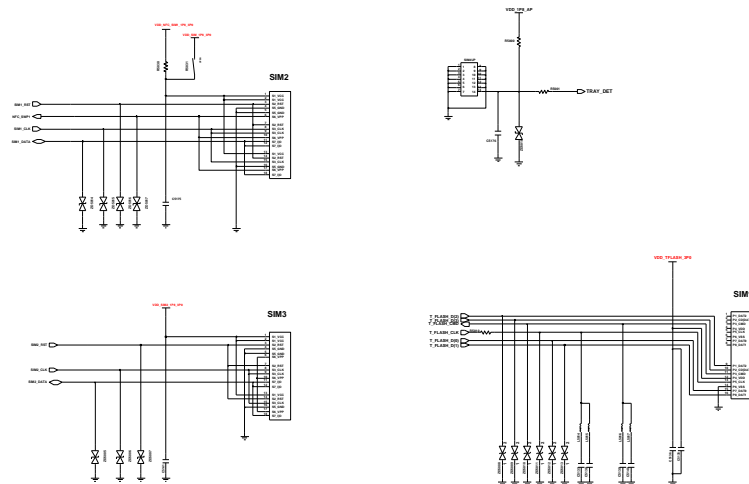
MAIN_PMIC



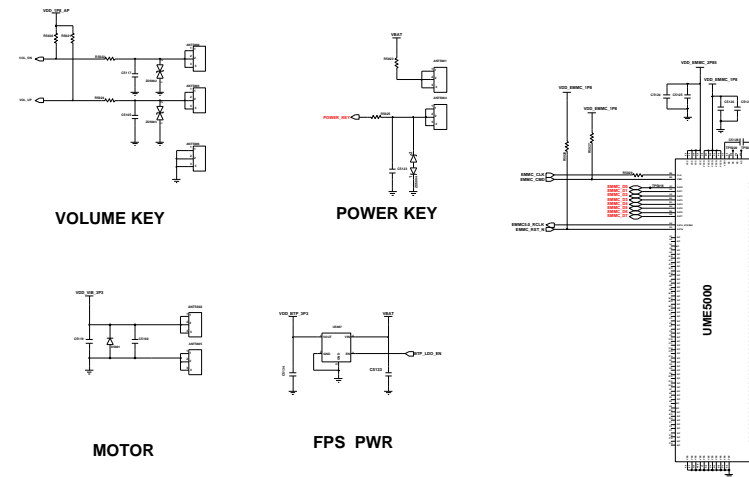
IF_PMIC



SIM & SD CARD (3IN3)



32GB eMMC 5.1



VOLUME KEY

POWER KEY

MOTOR

FPS PWR

NAME	VALUE
U5002-1	U5002-1
U5002-2	U5002-2
U5002-3	U5002-3
U5003	U5003
UME5000	UME5000

[illegible]

AUDIO

MAIN MIC

Sub MIC

RCV CONTACT

Ear MIC

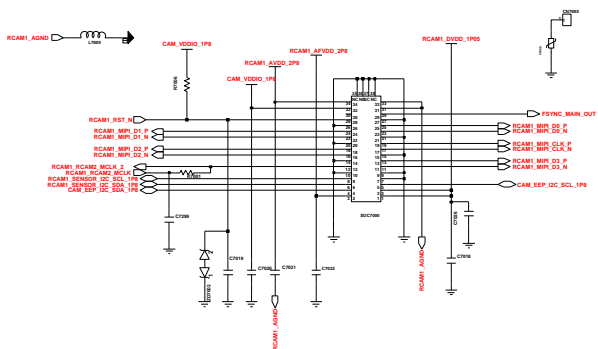
SPK AMP

Diagram showing the layout of an audio interface, including components like microphones, contacts, and an amplifier. The layout is divided into sections: MAIN MIC, Sub MIC, RCV CONTACT, Ear MIC, and SPK AMP. The diagram includes various electronic components, traces, and labels for different signals and power planes. A table at the bottom right provides additional information about the layout, including a legend and a table of components.

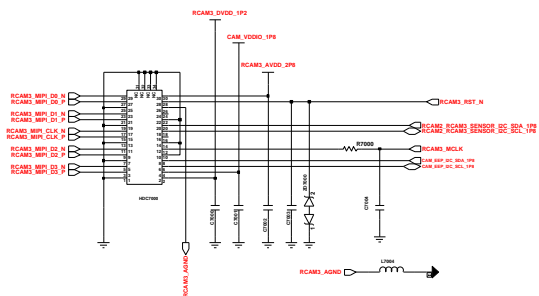
Legend	Component
AUDIO, SIMISO, SHIELD CAN	
VDD CORE	
VDD EXTEN CORE	
SPK AMP	
SPK AMP	

Engineer:	AUDIO, SIM/SD, SHIELD CAN				
Drawn by:					
R&D CHK:				TITLE:	Rev:
DOC CTRL CHK:					
MFG ENGR CHK:					
Page 1	QA CHK:	REV	Drawing Number: Page 4/7		

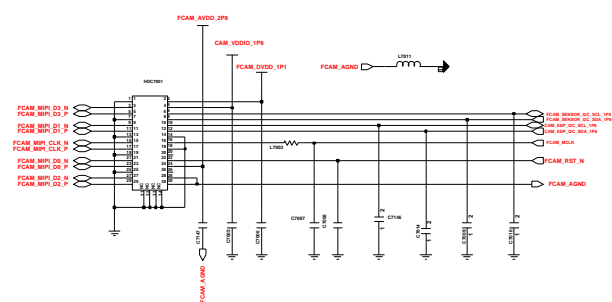
CAMERA_PART



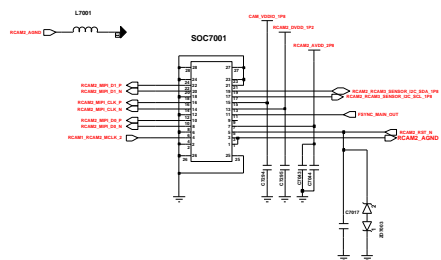
RCAM1_25M CAMERA(34PIN)_2X5



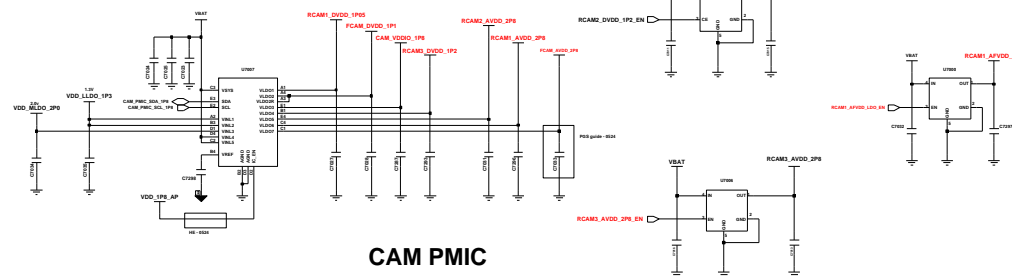
RCAM3_SUPER WIDE_8M CAMERA(30pin)_S5K4HA



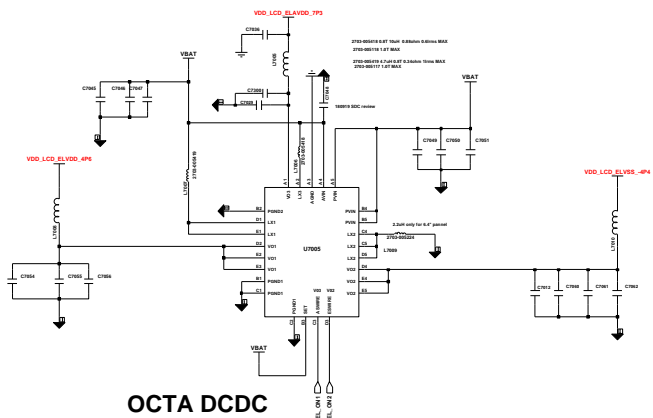
FRONT_16M CAMERA(30PIN)



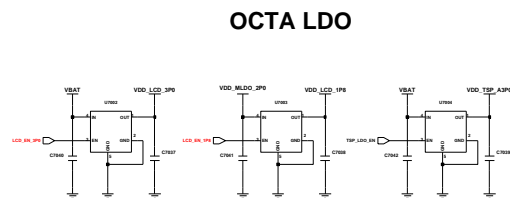
RCAM2_5M CAMERA(24PIN)_GC5035



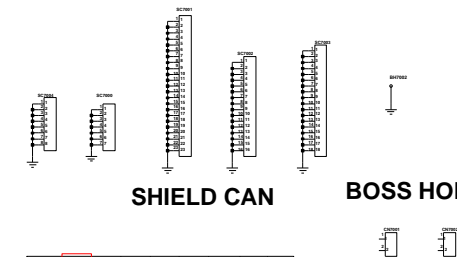
CAM PMIC



OCTA DCDC

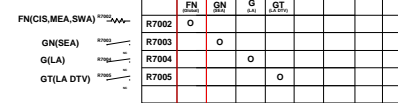


OCTA LDO



SHIELD CAN

BOSS HOLE



2D BARCODE

Engineer:	CAMERA, DISPLAY				
Drawn by:					
P&ID CODE:				TITLE:	Sheet:
DOC CTRL CODE:					
MFG ENG'D CODE:					
QA CTR:	REV:	Drawing Number:	Page: 07		

